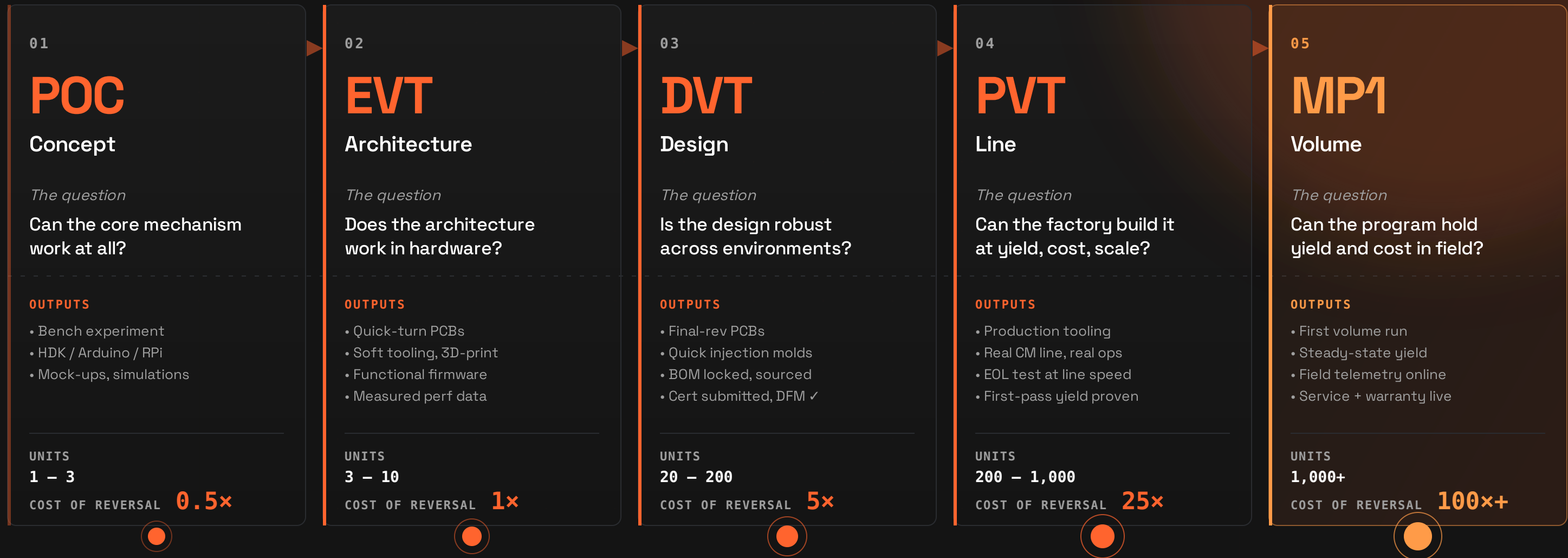


The Hardware Development Lifecycle

Five gates from concept to volume. Each a *decision*, not a test.



COST OF REVERSING A DECISION · SUPER-LINEAR

